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7. 101.001			FIRST MANGE INDEXES		200			
APPLICATION NO. 10/803,983	93/19/2004	FIRST NAMED INV				ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/803,983 03/19/2004 Mitsuhiro Shimamoto Q80572 6913 TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH A PLURALITY OF INTERNAL CIRCUITS OPERABLE IN SYNCHRONISM WITH INTERNAL CLOCK								
APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PU	BLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1400			\$300	\$1700	10/18/2005	
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NGUYEN, MINH T 281			-	327-295000				
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)								
PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.								
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NEC Electronics Corporation Kanagawa, Japan								
Please check the appropriate assignee category or categories (will not be printed on the patent):								
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Authorized Signature					Date	10-12-05		
Typed or printed name Howard L. Bernstein Registration No. 25, 665								
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